

CURRICULUM VITAE

183-A, Street No. 6, Jujhar Nagar
Patiala-147001 (Pb.)
Cell No.: 91-9779706457
Resi No.: 0175-2370009
E-mail: singh.amandeep183@gmail.com

AMANDEEP SINGH

CAREER OBJECTIVE:

Seeking a position as Design/Research/Operation Engineer, and to utilize my skills and abilities in the Industry/Institution that offers professional growth while being resourceful, innovative and flexible.

EDUCATIONAL QUALIFICATION:

2015 -	Ph. D in ECE (pursuing) Punjabi University, Patiala, Punjab Thesis Topic - Development of an Optimized Authentication Technique Using Efficient Key Management for Long Term Evolution Networks
2012 – 2013	PGDCA with 67% (correspondence) Punjabi University, Patiala
2008 – 2010	M.Tech in VLSI Design & CAD with 7.00 CGPA Thapar University, Patiala Thesis Topic - FPGA implementation and analysis of DES and TWOFISH encryption algorithms
2004 – 2007	B.Tech in ECE with 70.8%

SUSCET, Tangori

Mohali (Pb.)

2000 – 2003

Diploma in ECE with 61%

Doaba Polytechnic, Raipur

Roopnagar (Pb.)

SOFTWARE PROFICIENCY:

Design Tools : Mentor Graphics, Xilinx tools for FPGA (ISE, Modelsim)
Programming Languages : VHDL, Verilog, C, Matlab, Assembly language for x85
Operating Systems : Windows XP, Linux

PROFESSIONAL TRAININGS:

- Six months Industrial Training (Jan 2006 to June 2006) at Semiconductor Complex Limited (SCL), Mohali
- Six weeks Industrial Training (June 2005 to July 2005) at Bharat Sanchar Nigam Limited (BSNL), Patiala
- Certificate Course in Cisco Routers Administration (June 2006 to July 2006) at IIHT, Bangalore.
- Ten days course (Dec 2009) in IEP (VLSI) at Thapar University, Patiala.
- Short term course on “FPGA signal processing of Cognitive Radios” at Thapar University, Patiala from June 11 to June 15, 2012.
- Short term course on “Cyber Threat Detection & Safety”, at Punjabi University, Patiala from Jan 04 to Jan 15, 2016.
- Short term course on “Wireless Communication”, at Punjabi University, Patiala from May 02 to May 06, 2016.

WORKING EXPERIENCE:

1. Lecturer at Thapar Polytechnic, Patiala

Total Period : Approx. 1 year
Subjects Taught : DE, EDC, PE, Micro- Processors, Consumer Electronics

2. Assistant Professor at UCOE, Punjabi University, Patiala

Total Period : From July 2010 to till working
Subjects Taught : Power Electronics, MES, ADSD, Signals & Systems

RESEARCH PAPERS:

National Conferences : 7 papers
International Journals : 42 papers

MEMBERSHIP

Member of International Association of Engineers

PERSONAL DETAILS:

Father's Name : S. Pritpal Singh Bhandari
Occupation : Pb. Govt. Employee
Mother's Name : Smt. Veeran Wanti
Occupation : Pb. Govt. Employee
Date of Birth : 20th Feb, 1985
Nationality : Indian
Marital Status : Married
Language Known : English, Punjabi, Hindi

(Amandeep Singh)